## ECE2504 Design Project 3 Cover Sheet

Student Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_



Honor Code Pledge:

I have neither given nor received unauthorized assistance   
on this assignment (sign in the box to acknowledge this):

|  |  |  |
| --- | --- | --- |
| **Project Item** | **Value** | **Points** |
| **Project Questions:** |  |  |
| * Table showing the opcode values that were chosen | 5 |  |
| * Step 1 simulation results. | 5 |  |
| * Completed table of dissembled instructions (Table 3) | 5 |  |
| * Compare the results of the simulation to what was expected given the instructions in Table 3 | 5 |  |
| * Clearly documented changes made to the design | 5 |  |
| * Annotated simulation results in your report showing the correct behavior of the augmented Step 2 instruction | 10 |  |
| * A table, similar to Table 4, that contains the information necessary to understand the simulation results for each instruction | 5 |  |
| **Report:** |  |  |
| * Cover sheet | 5 |  |
| * Organization: Clear, concise presentation of content; Use of appropriate, well-organized sections | 10 |  |
| * Design approach, decisions, observations and conclusions | 5 |  |
| * Implementation discussion: Choice of methods for implementing instructions, opcode binary assignments for instructions, changes to the design. | 10 |  |
| * Supporting files: instruction.txt, data.txt, and modified Verilog files | 5 |  |
| **Validation** |  |  |
| * All steps function correctly | 25 |  |
| * **TOTAL POINTS** |  |  |
| **Extra Credit**: Correctly implementing optional instruction | 10 |  |